



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Chiharu IRIGUCHI

Application No.: 10/790,221

Docket No.: 118580

Filed: March 2, 2004

For: ALIGNER, EXPOSING METHOD, METHOD FOR MANUFACTURING THIN-FILM TRANSISTOR, DISPLAY DEVICE, AND ELECTRONIC DEVICE USING SHADING MEANS

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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JAO:MB/al

Date: June 2, 2004

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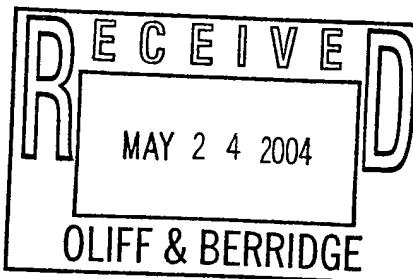


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/790,221	03/02/2004	1756	770	118580	9	13	3

25944
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CONFIRMATION NO. 9847
FILING RECEIPT



OC000000012718164

Date Mailed: 05/20/2004

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Applicant(s)

Chiharu Iriguchi, Suwa-shi, JAPAN;

Assignment For Published Patent Application

SEIKO EPSON CORPORATION, TOKYO, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-072092 03/17/2003

If Required, Foreign Filing License Granted: 05/19/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Aligner, exposing method, method for manufacturing thin-film transistor, display device, and electronic device using shading means

Preliminary Class

430

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